

PCT

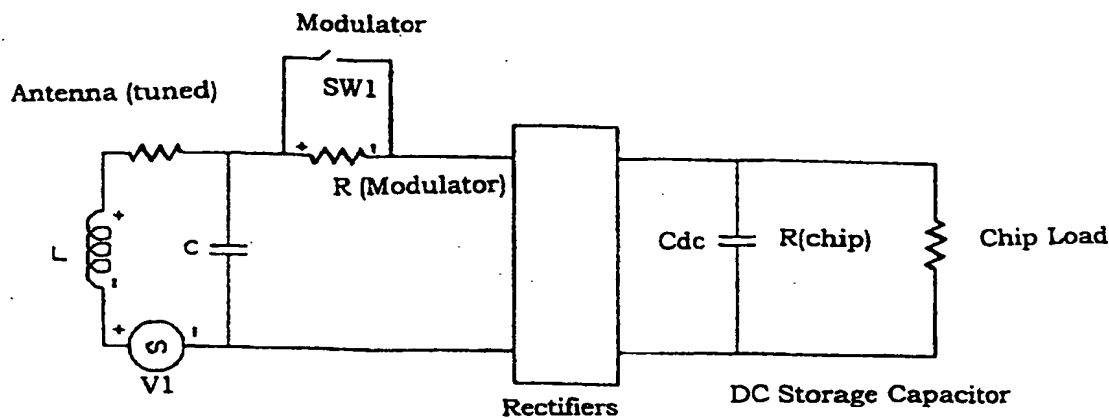
WORLD INTELLECTUAL PROPERTY ORGANIZATION  
International Bureau



INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification <sup>6</sup> : <b>H04B 1/59, G01S 13/74</b>		A1	(11) International Publication Number: <b>WO 99/39450</b>
			(43) International Publication Date: 5 August 1999 (05-08.99)
(21) International Application Number: PCT/AU99/00059		(81) Designated States: AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, DE, DK, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, UA, UG, US, UZ, VN, YU, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).	
(22) International Filing Date: 29 January 1999 (29.01.99)			
(30) Priority Data: PP 1545 29 January 1998 (29.01.98) AU PP 1730 9 February 1998 (09.02.98) AU			
(71) Applicant (for all designated States except US): MAGELLAN TECHNOLOGY PTY. LTD. [AU/AU]; 65 Johnston Street, Annandale, NSW 2038 (AU).			
(72) Inventor; and (75) Inventor/Applicant (for US only): MURDOCH, Graham [AU/AU]; 65 Johnston Street, Annandale, NSW 2038 (AU).		Published With international search report.	
(74) Agent: BALDWIN SHELSTON WATERS; 60 Margaret Street, Sydney, NSW 2000 (AU).			

(54) Title: A TRANSCEIVER



**Invention with Modulator in AC part of Circuit**

(57) Abstract

A transceiver according to the invention has application to a baggage handling system, amongst others, and includes an electrical circuit having a modulated series resistor, in the form of resistor R(modulator). Also included is a parallel switch SW1, which is placed between the antenna and a storage capacitor in the AC part of the circuit. In other embodiments the modulated series resistor, again in the form of resistor R(modulator) and parallel switch SW1, is placed between the antenna and the storage capacitor in the DC part of the circuit. Both circuits produce the same transmitted signal, although in practice the latter is simpler to implement due to its DC operating bias. In still further embodiments a modulated series resistor is placed between the antenna and the antenna's tuning capacitor.